

Electronic Acknowledgement Receipt

EFS ID:	3574214
Application Number:	10720764
International Application Number:	
Confirmation Number:	3114
Title of Invention:	Semiconductor integrated logic circuit including two pmos transistors connected in series and two nmos transistors connected in series
First Named Inventor/Applicant Name:	Masashi Yonemaru
Customer Number:	23117
Filer:	Leonidas Boutsikaris/Tanja Majca
Filer Authorized By:	Leonidas Boutsikaris
Attorney Docket Number:	829-618
Receipt Date:	07-JUL-2008
Filing Date:	25-NOV-2003
Time Stamp:	16:06:25
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	08290618Transmittal.pdf	68408 42579b68deaff6a12a4726f885afab932e7725e9	no	1

Warnings:

Information:

2		08290618Amendment.pdf	146749 <small>49baad79000a57648547846ce938cb35 ac7199a7</small>	yes	13
Multipart Description/PDF files in .zip description					
		Document Description	Start	End	
		Amendment After Final	1	1	
		Claims	2	9	
		Applicant Arguments/Remarks Made in an Amendment	10	13	
Warnings:					
Information:					
Total Files Size (in bytes):			215157		
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